

1  
FIG.

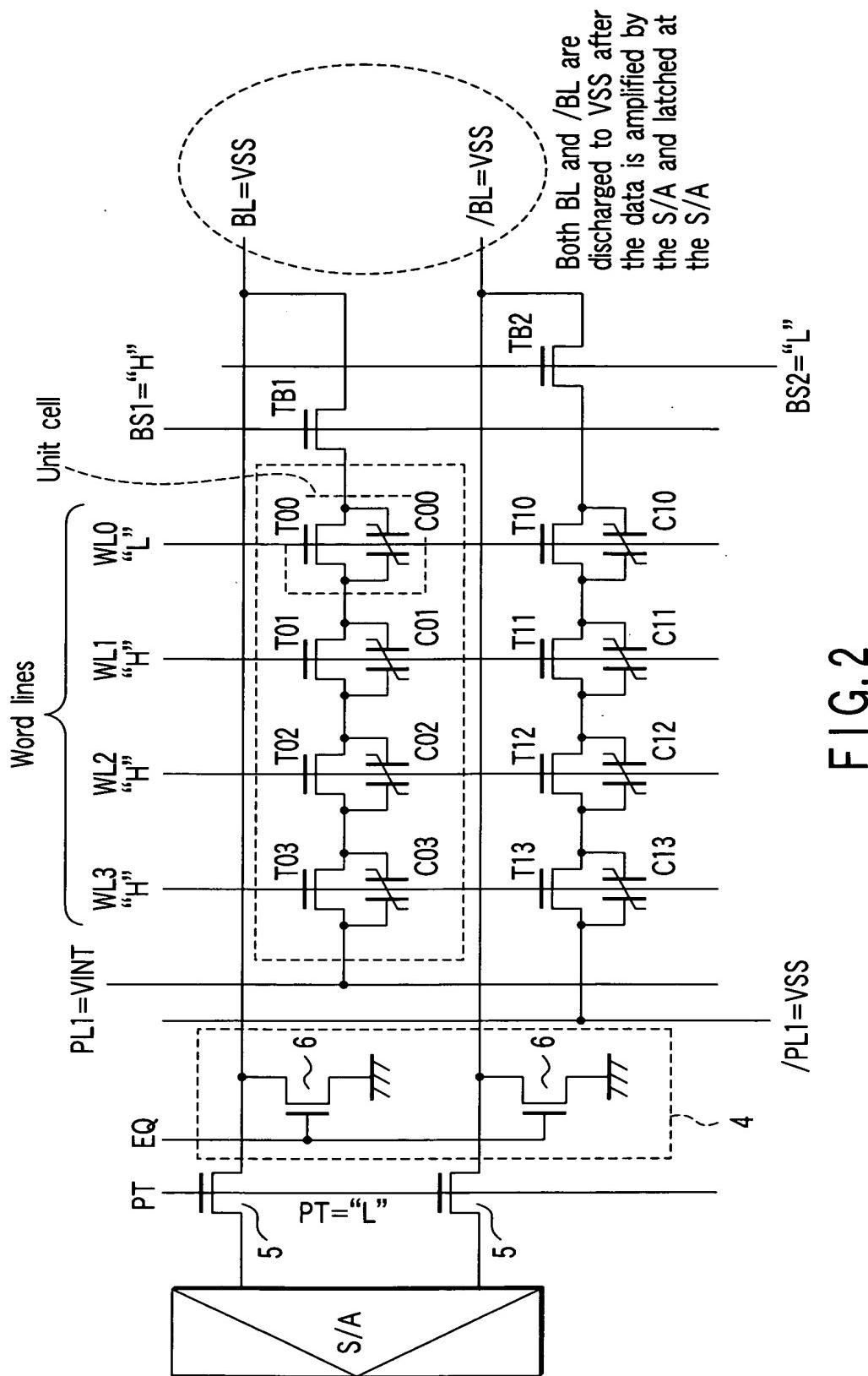


FIG. 2

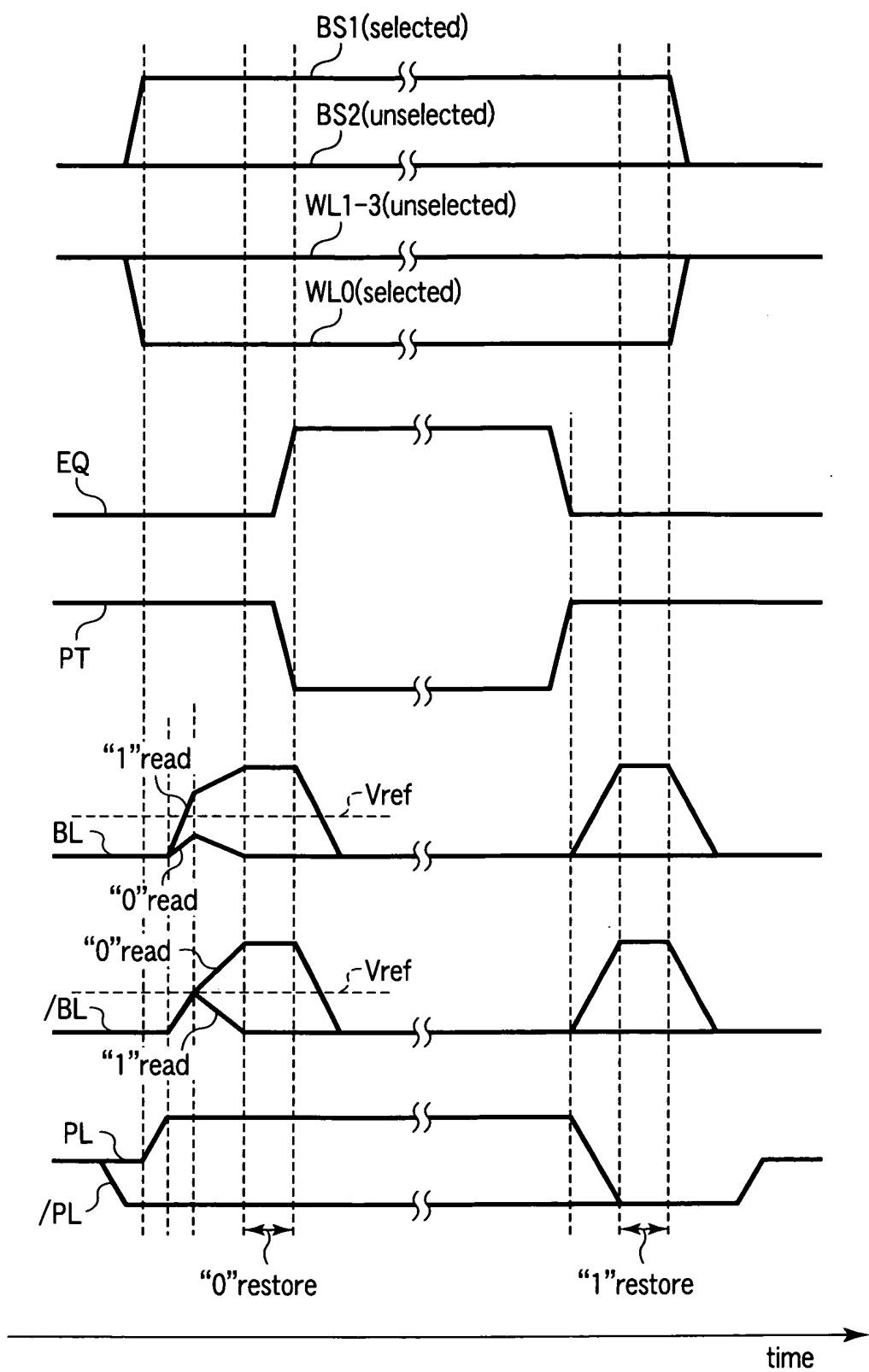


FIG. 3

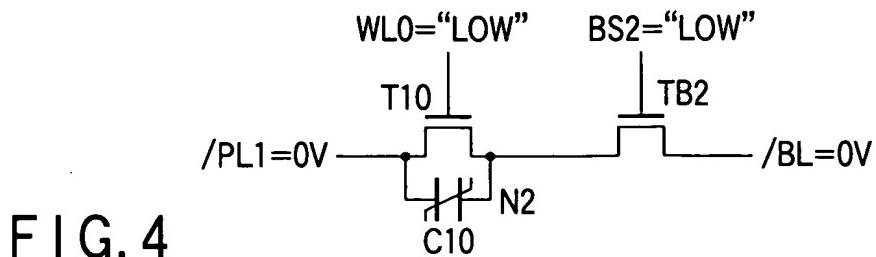


FIG. 4

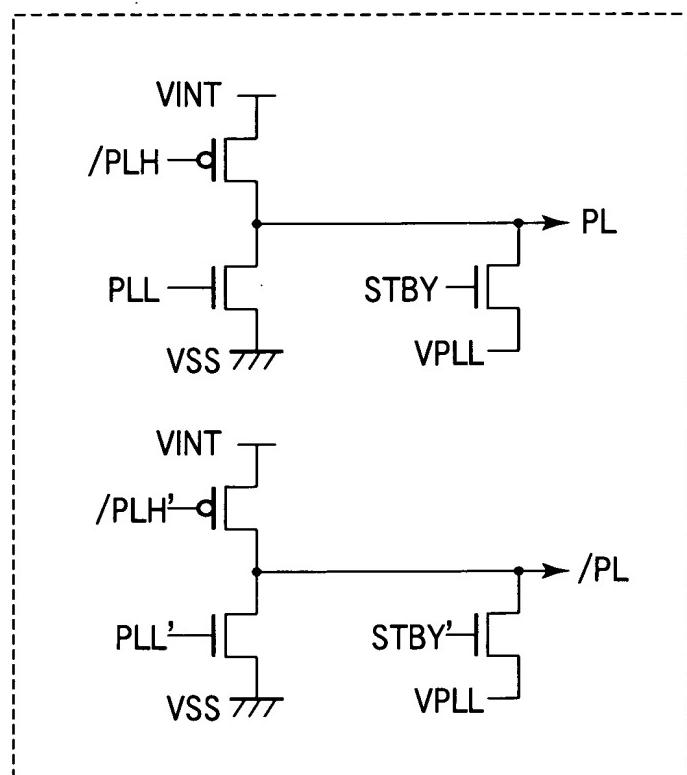


FIG. 5

Plate-line potential control circuit

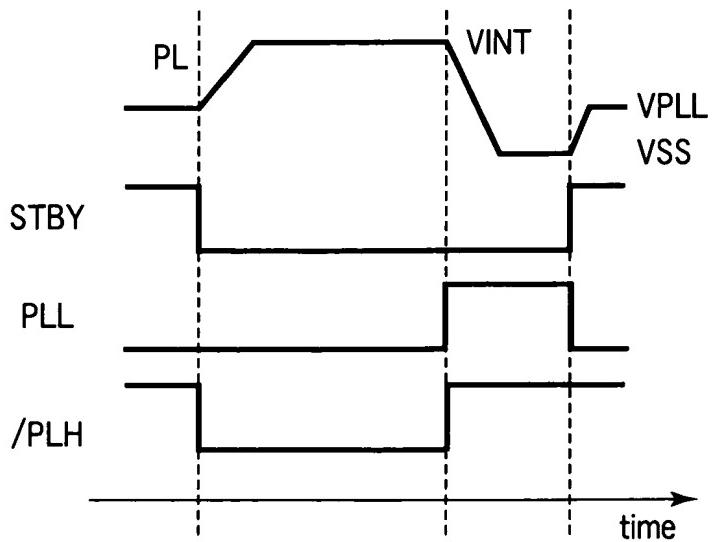


FIG. 6

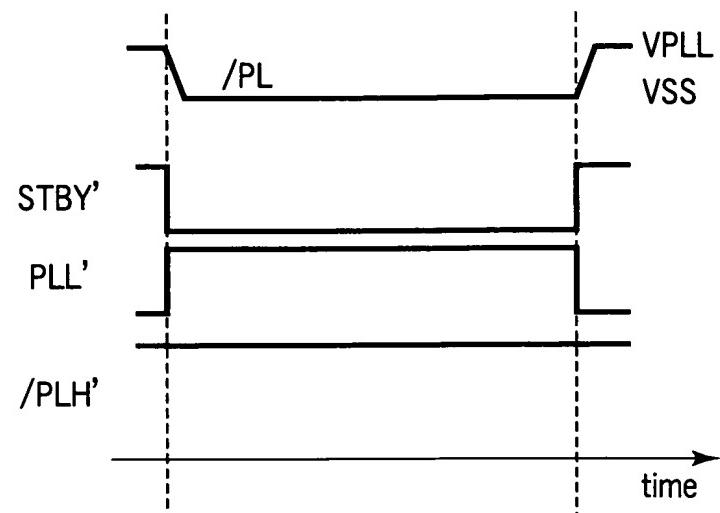


FIG. 7

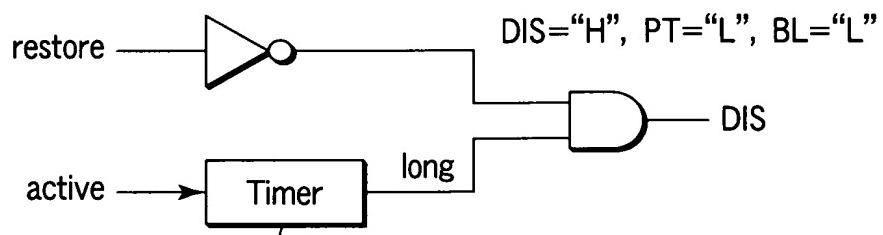


FIG. 8

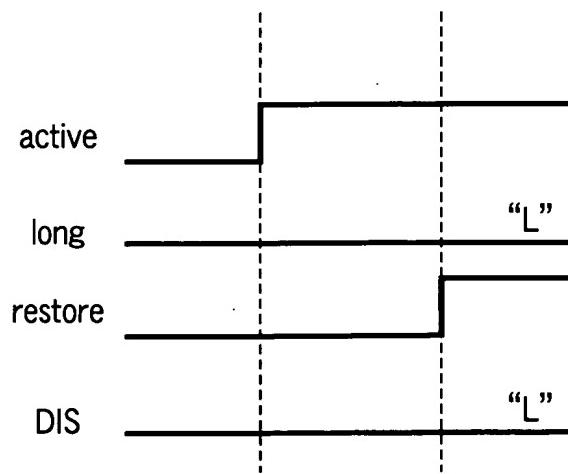
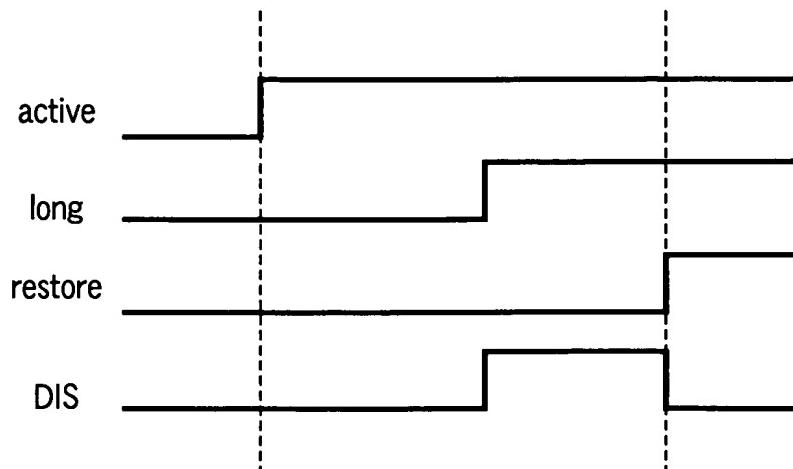


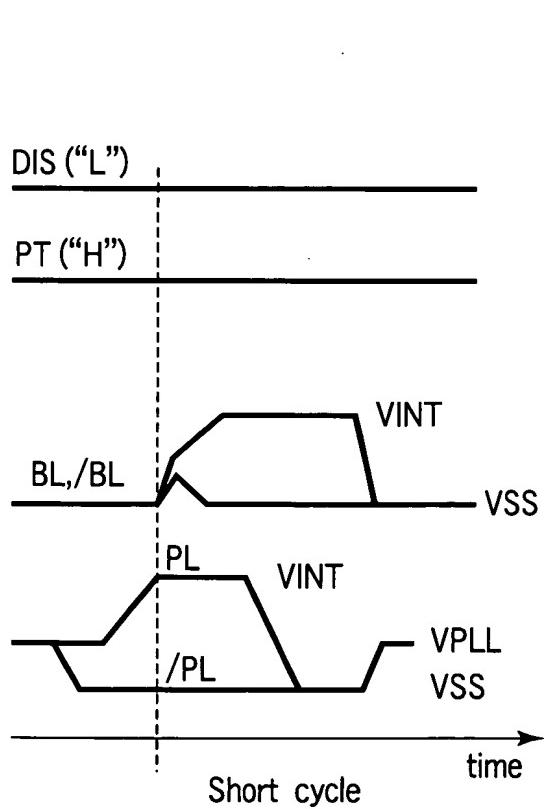
FIG. 9

Short time from active to restore

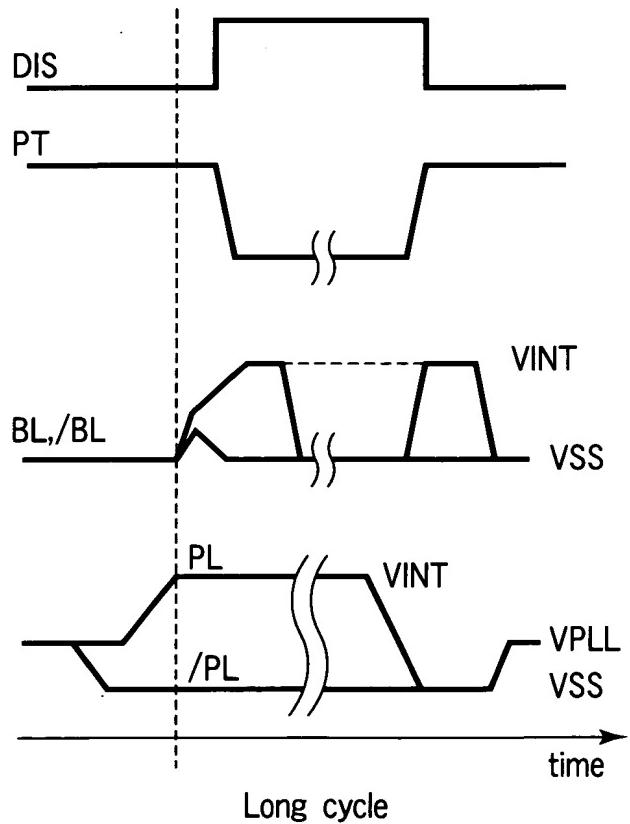


Long time from active to restore

FIG. 10



Short cycle



Long cycle

FIG. 11

FIG. 12

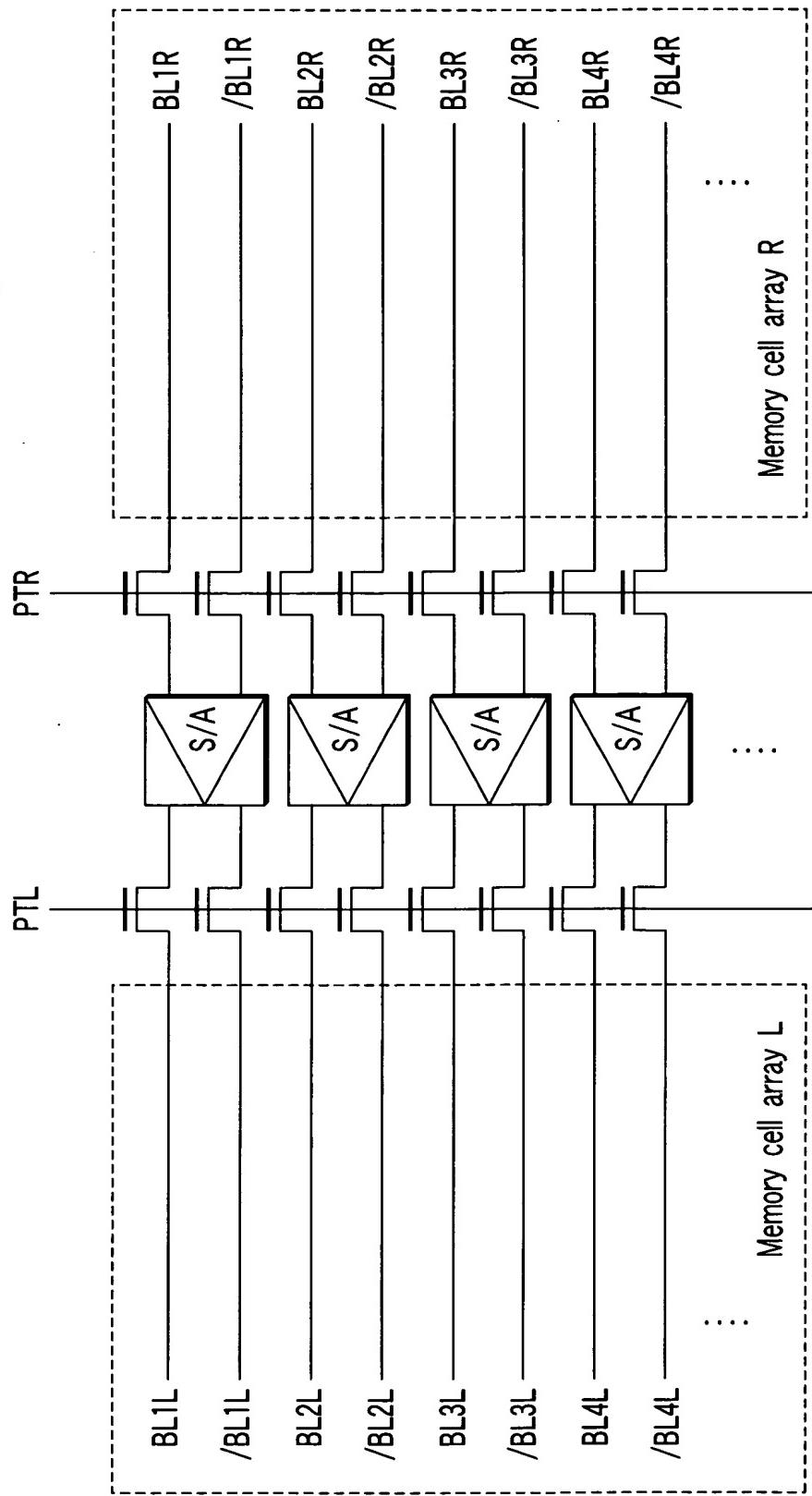
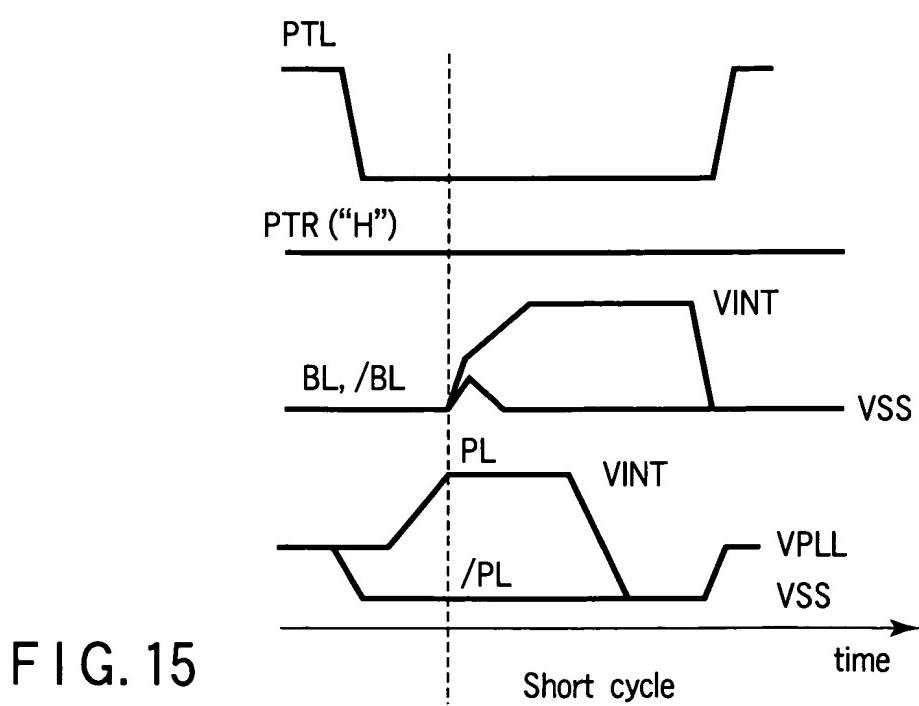
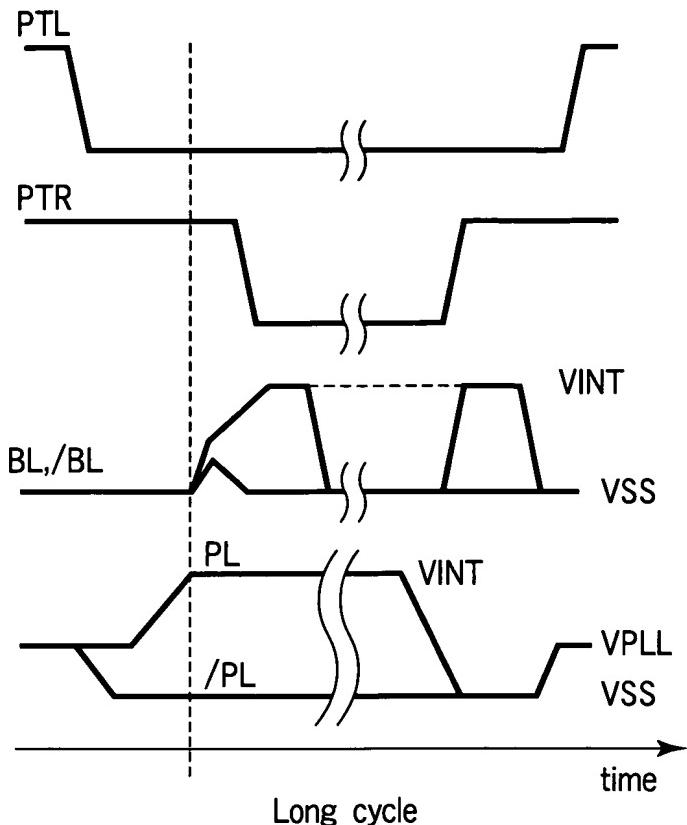


FIG. 13



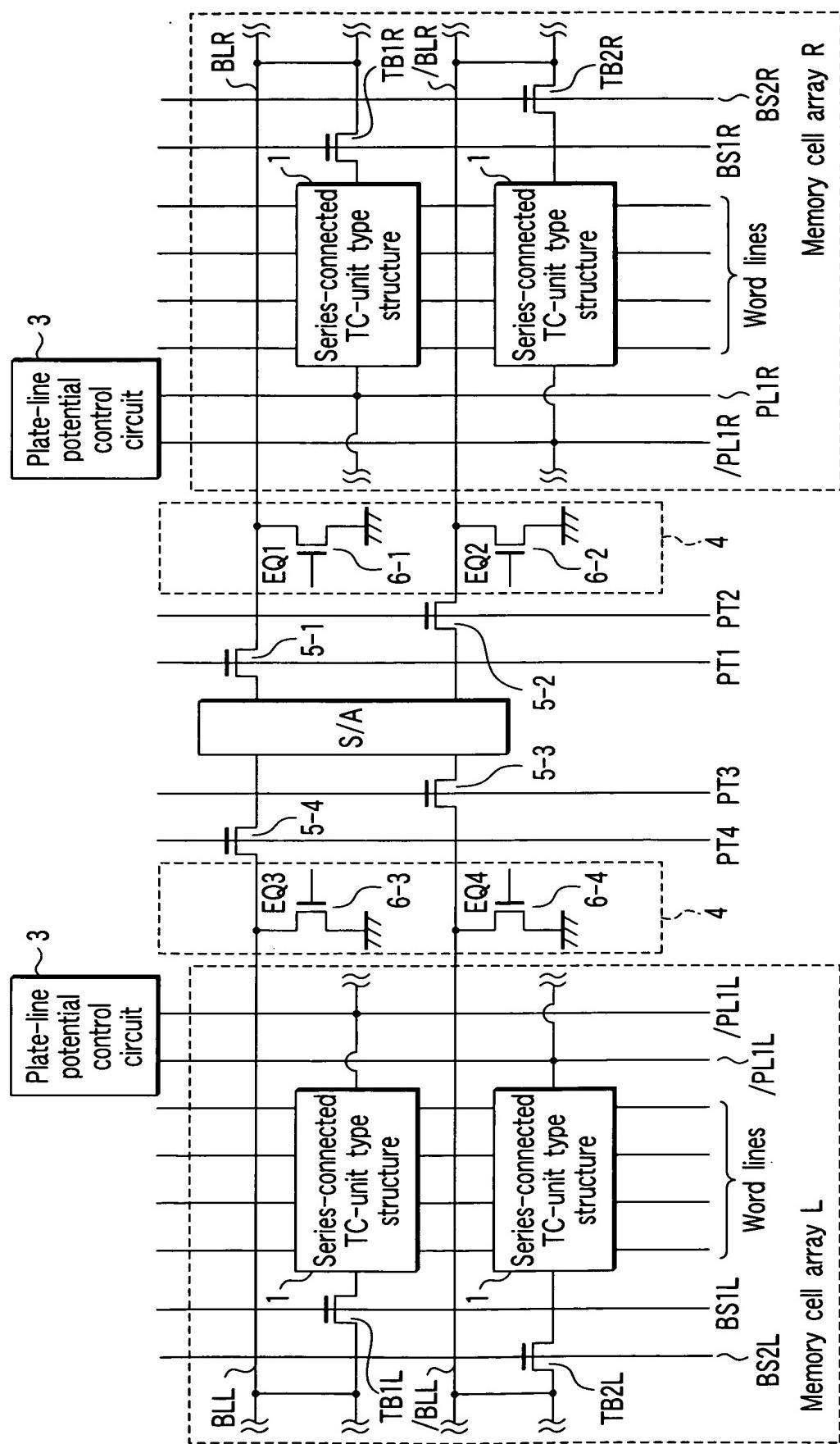


FIG. 16

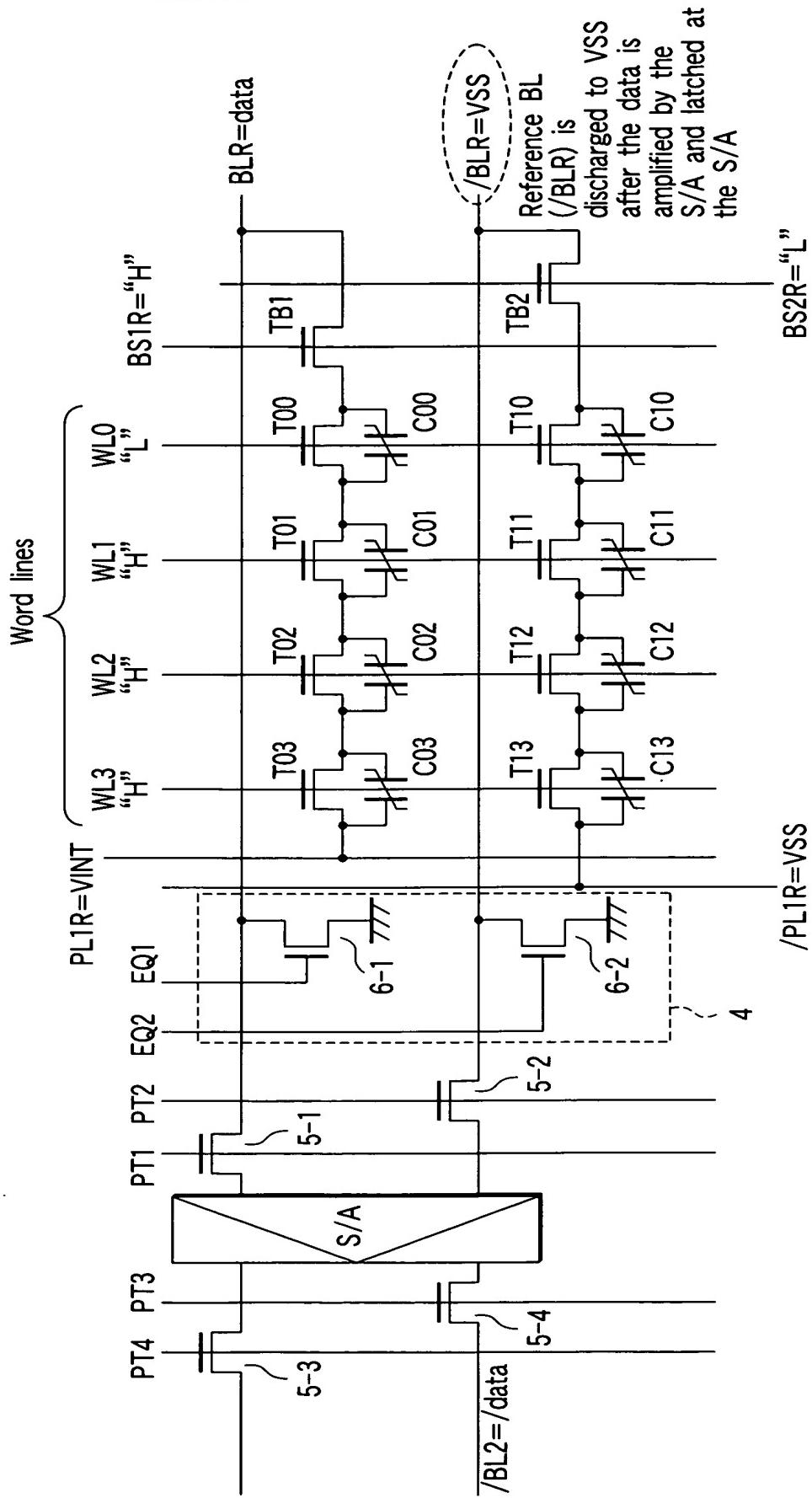


FIG. 17

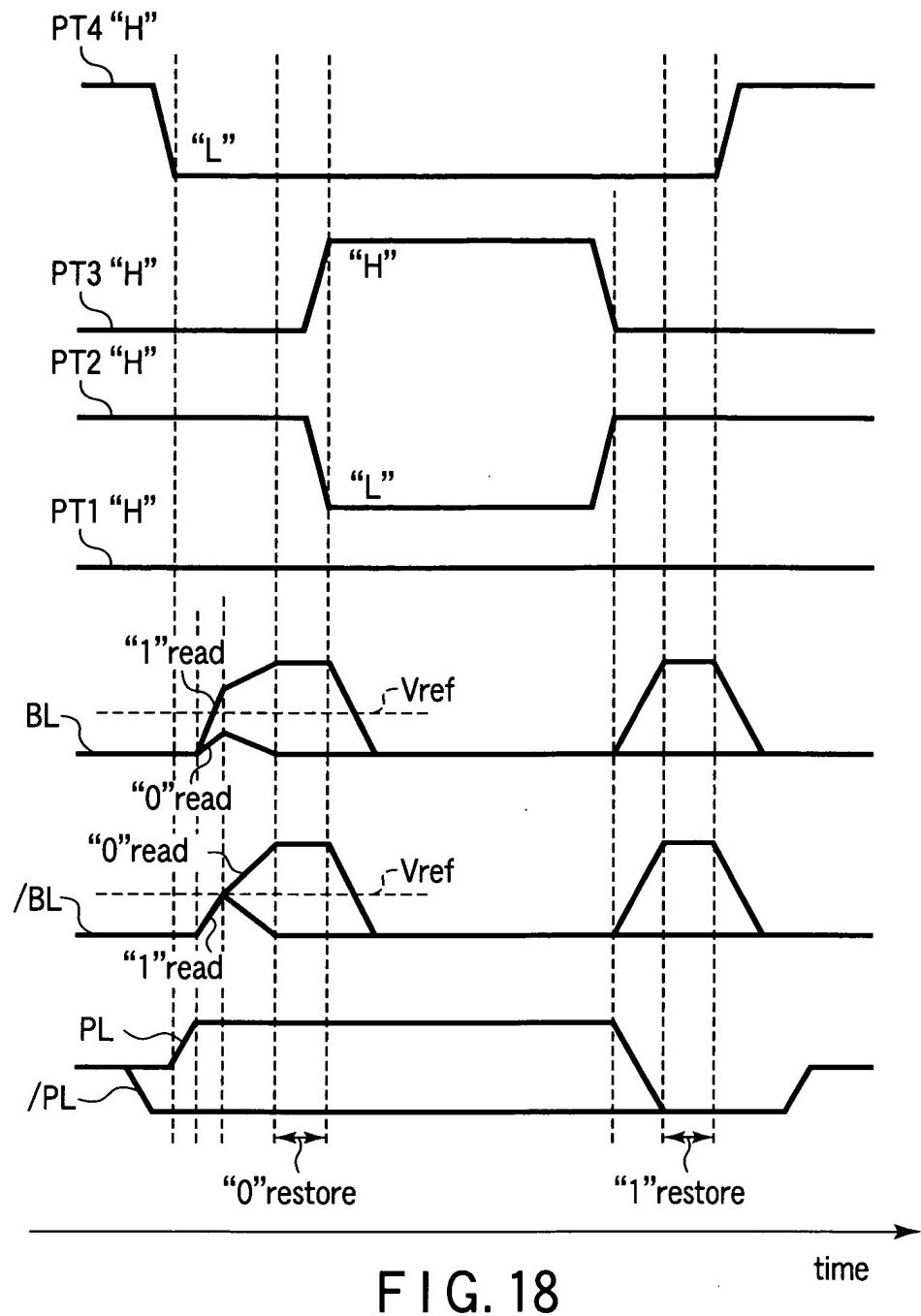


FIG. 18

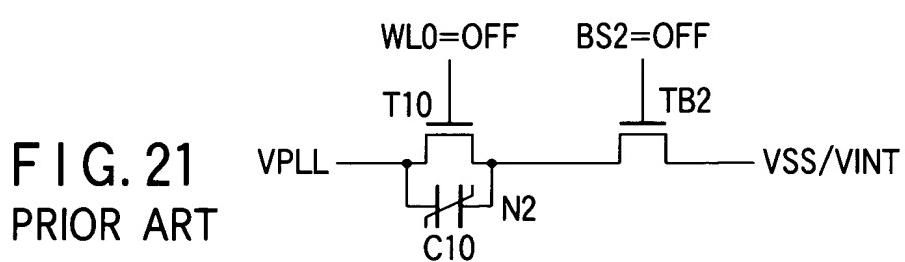


FIG. 21  
 PRIOR ART

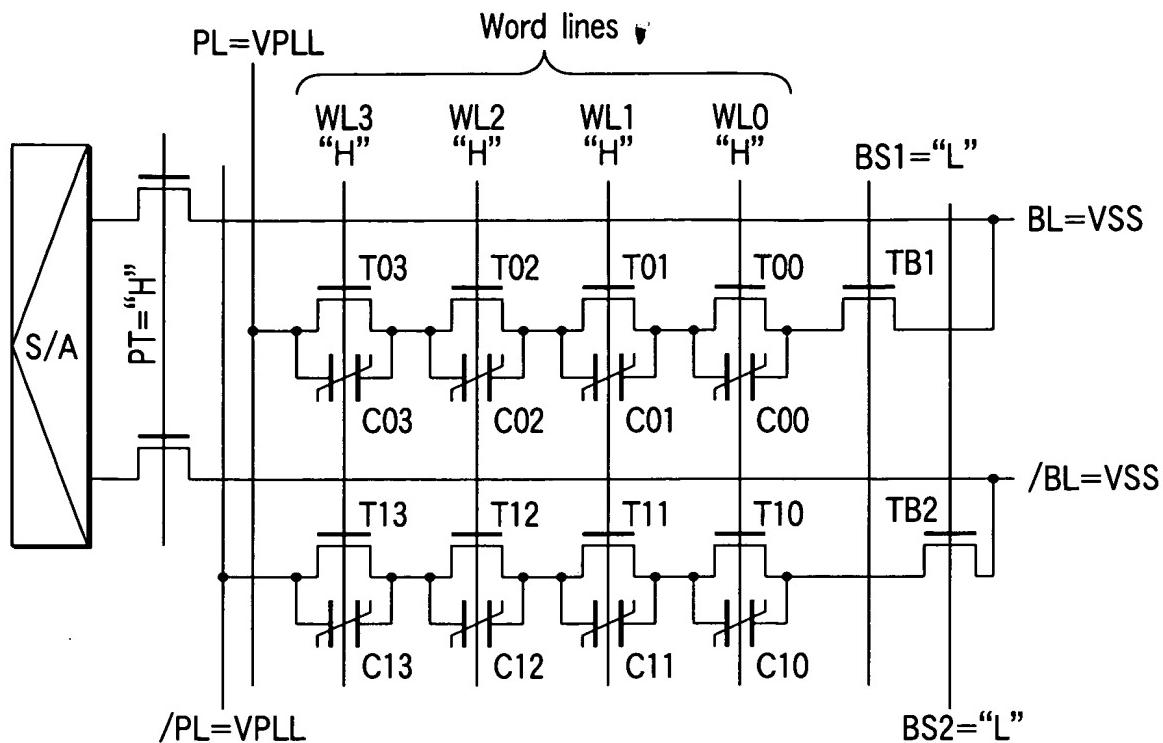


FIG. 19 PRIOR ART

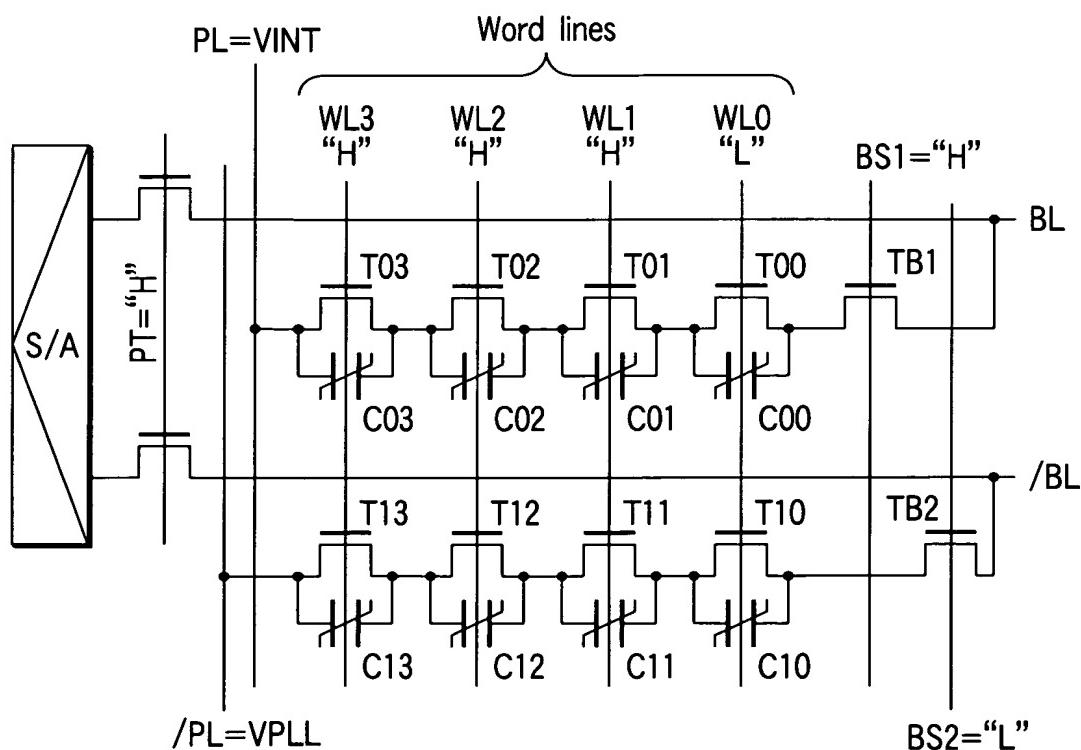


FIG. 20 PRIOR ART